### Amendments to the Drawings

The attached Replacement Sheets of drawings includes changes to Figures 4, 12, 13, 14, 15, and 32 addressing the Examiner's objections. In each of the figures, previously omitted descriptive wording has been added. No new matter has been added. In addition to the replacement sheets, formal replacement sheets are supplied for all other sheets.

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s):

Epps, Garry P.; Laor, Michael

Assignee:

Cisco Technology, Inc.

Title:

Pipelined Packet Switching And Queuing Architecture

Serial No.:

09/503,552

Filing Date:

February 14, 2000

Examiner:

Kevin C. Harper

Group Art Unit:

2666

Docket No.:

CIS0021US

Client Ref. No.:

1506

RECEIVEDNovember 22, 2004

Mail Stop Amendment Attention: Official Draftsperson COMMISSIONER FOR PATENTS P. O. Box 1450 Alexandria, VA 22313-1450

Technology Center 2600

## **SUBMISSION OF FORMAL DRAWINGS**

Dear Sir:

Applicants submit twenty-seven (27) sheets of formal drawings, consisting of Figures 1, 2, 3, 4, 5, 6, 7, 8, 9, 10, 11, 12, 13, 14, 15, 16 & 19, 17, 18, 20 & 21, 22 & 23, 24 & 25, 26, 27, 28, 29, 30 & 31, and 32; in the above-named application. If there are any questions regarding these drawings, please call the undersigned at (512) 439-5085.

I hereby certify that this correspondence is being deposited with the United States Postal Service as First Class Mail in an envelope addressed to Attention: Mail Stop Amendment, Attn: Official Draftsperson, Commissioner For Patents, P. O. Box 1450, Alexandria, VA 22313-1450, on November 22, 2004.

Attorney for Applicant(s)

Date of Signature

Respectfully submitted,

Marc R. Ascolese

Attorney for Applicants

Reg. No. 42,268

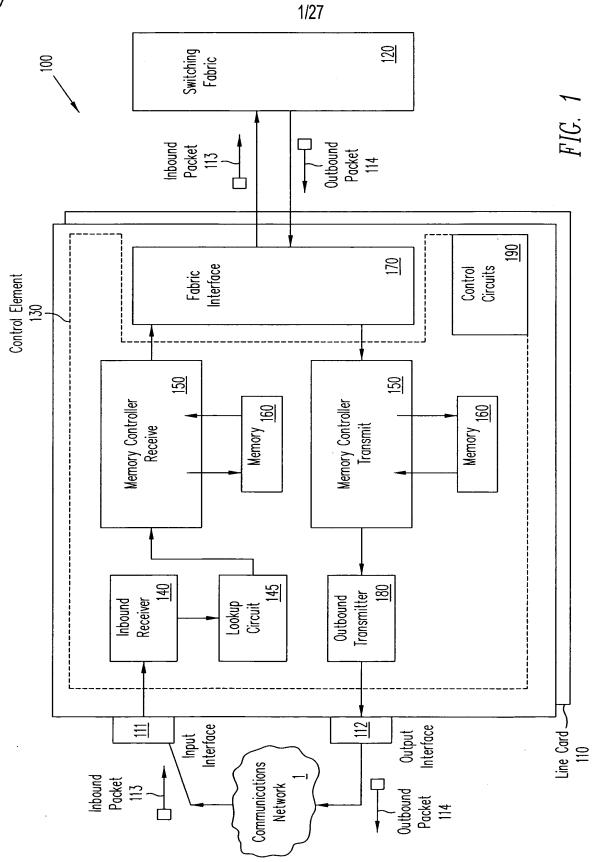
(512) 439-5080

(512) 439-5099 (Facsimile)

Application Serial No.: 09/503,442

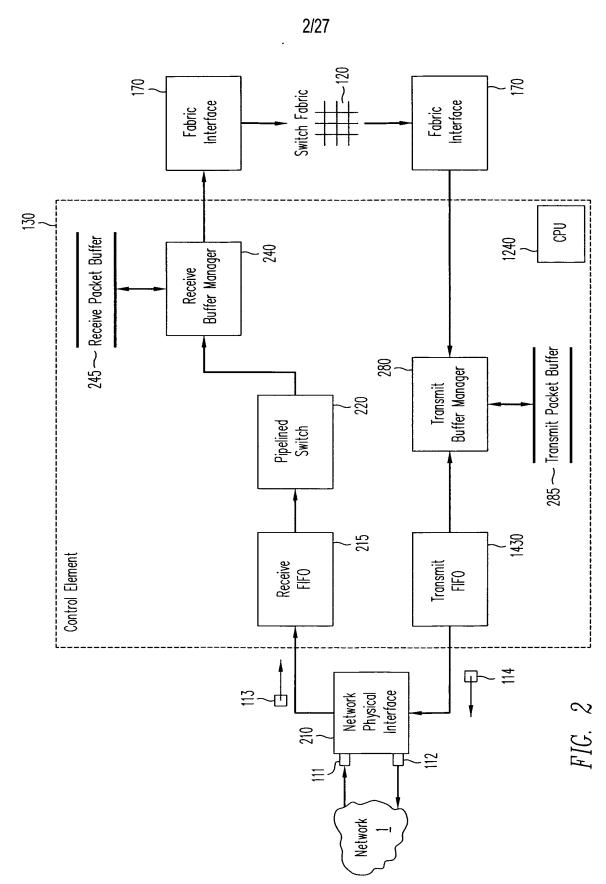
First Named Inventor: Garry P. Epps
Title: Pipelined Packet Switching And Queuing Architecture



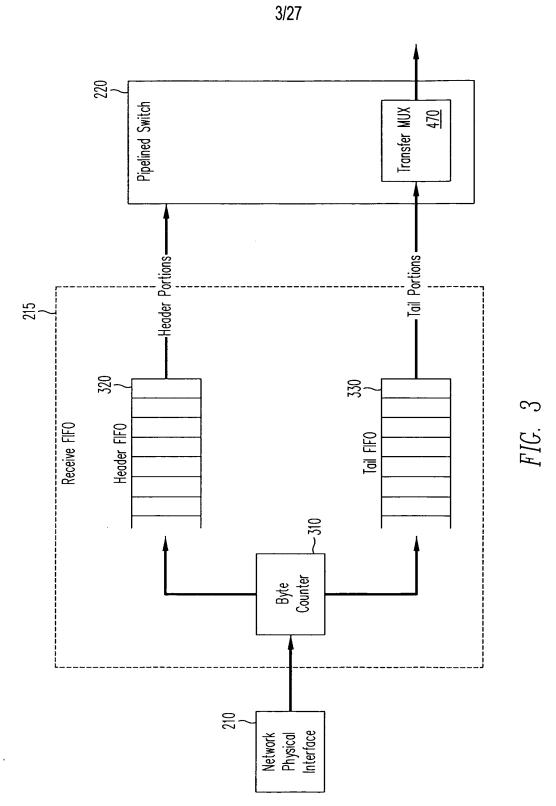


Application Serial No.: 09/503,442

First Named Inventor: Garry P. Epps
Title: Pipetined Packet Switching And Queuing Architecture

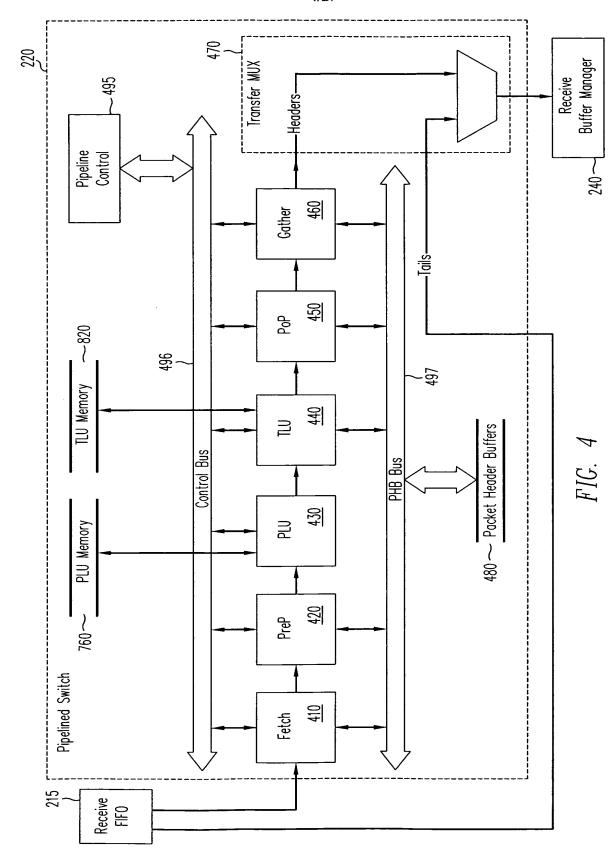


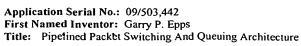






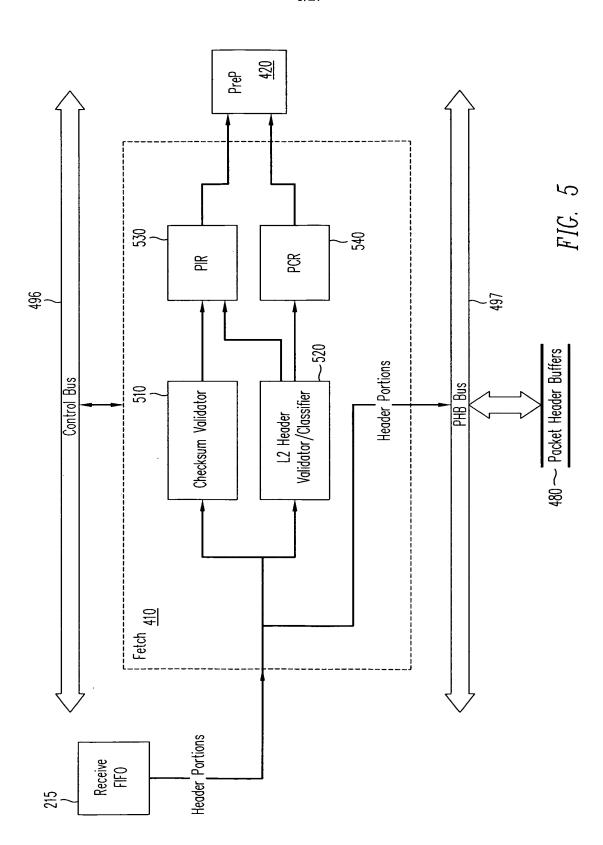






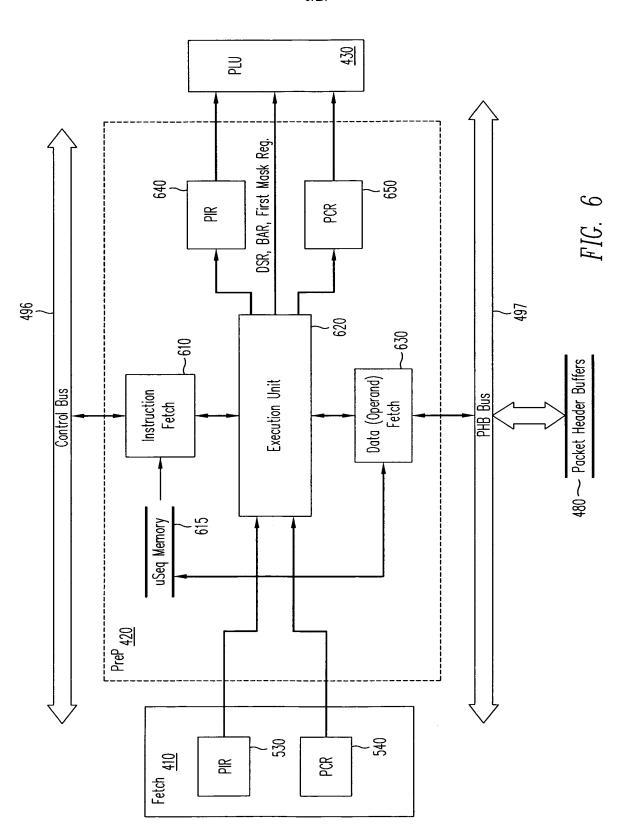
REPLACEMENT SHEET



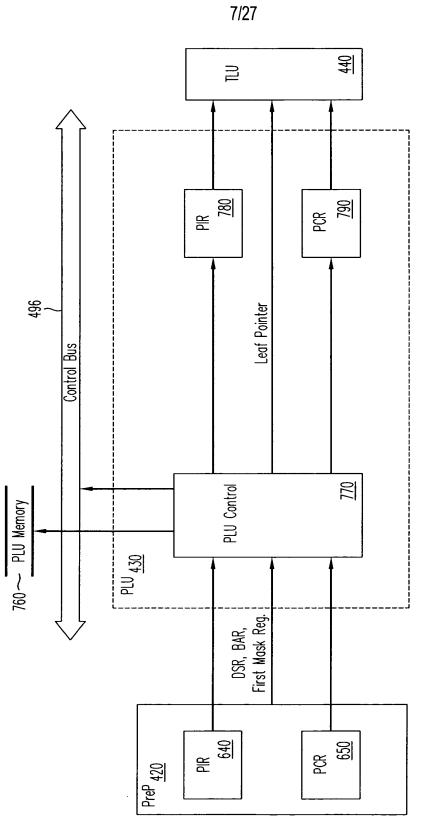




REPLACEMENT SHEET

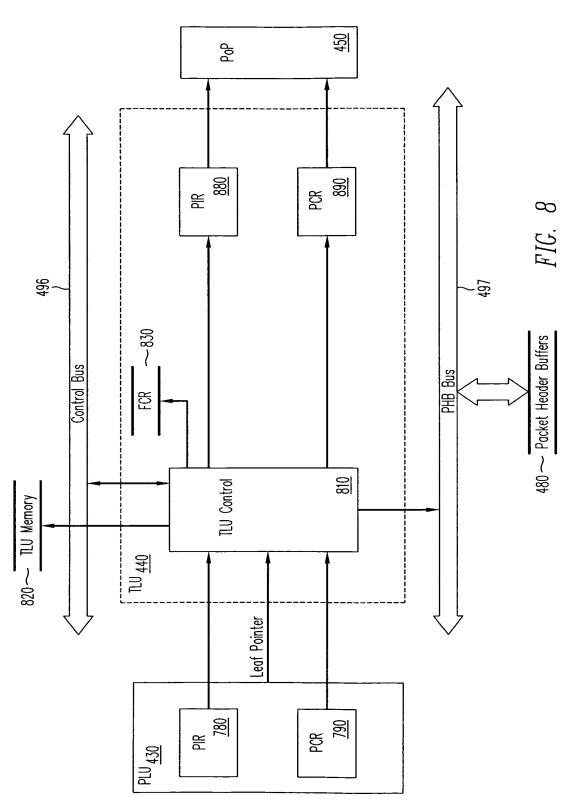






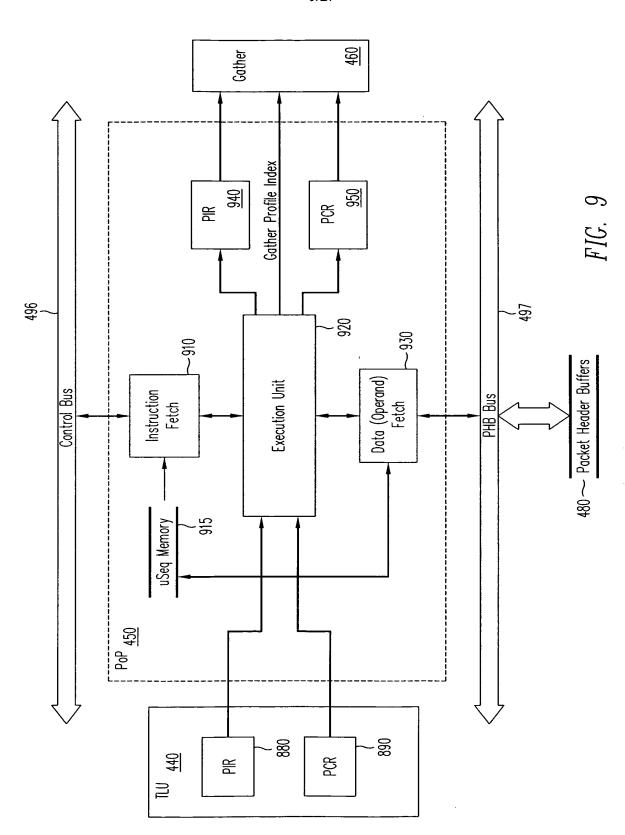






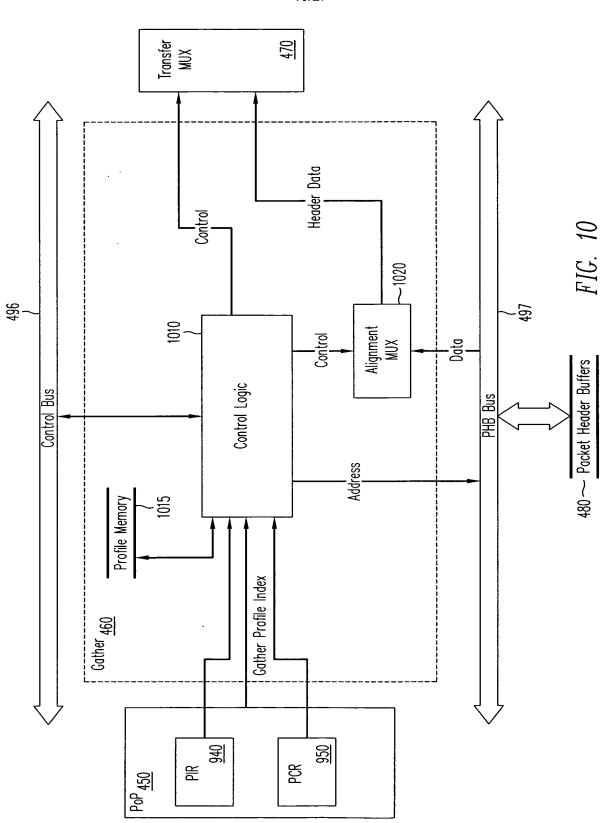


REPLACEMENT SHEET



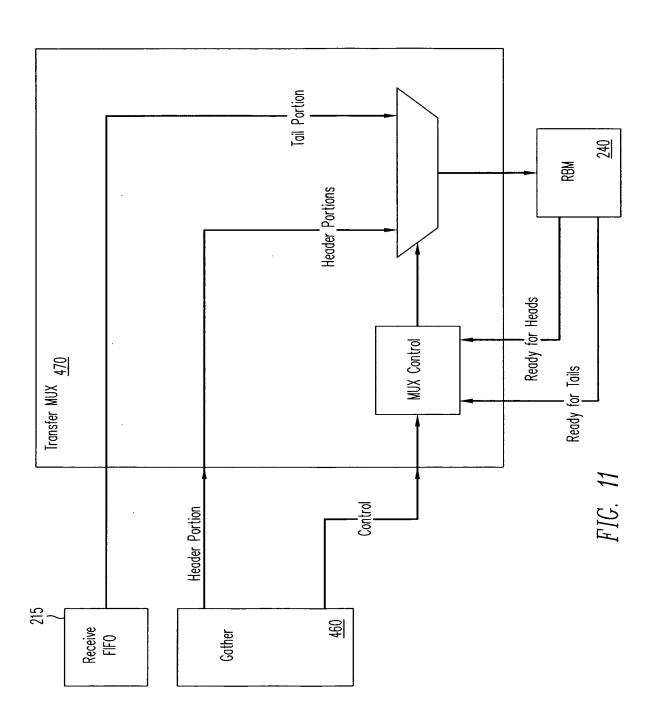






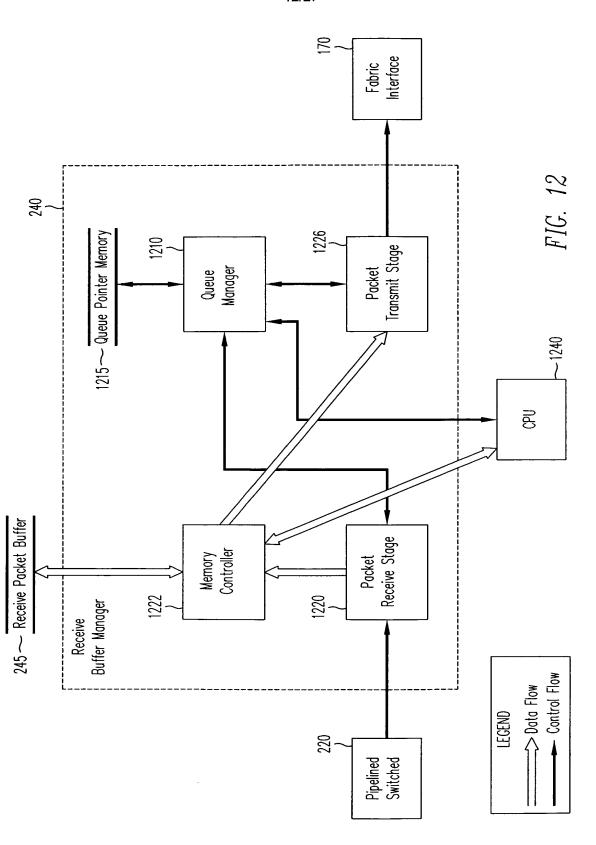


REPLACEMENT SHEET



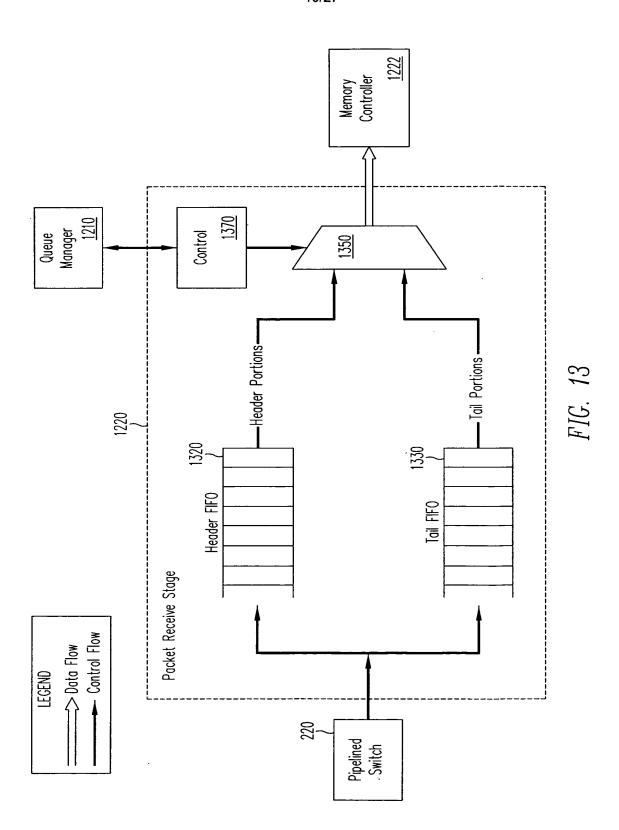






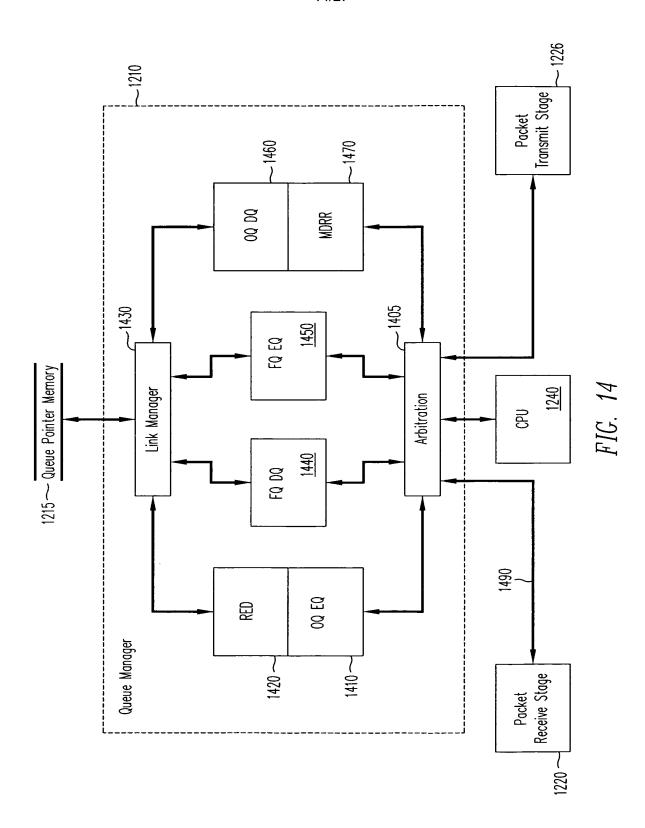


13/27



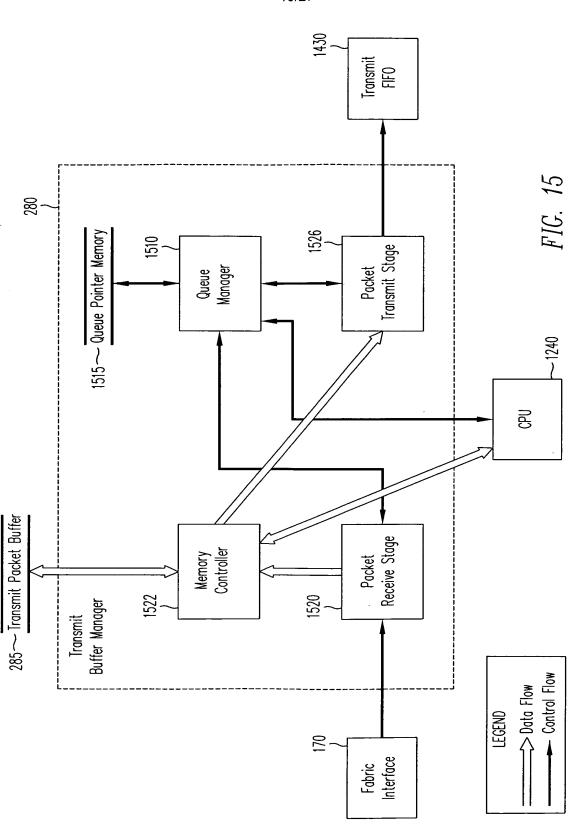


14/27



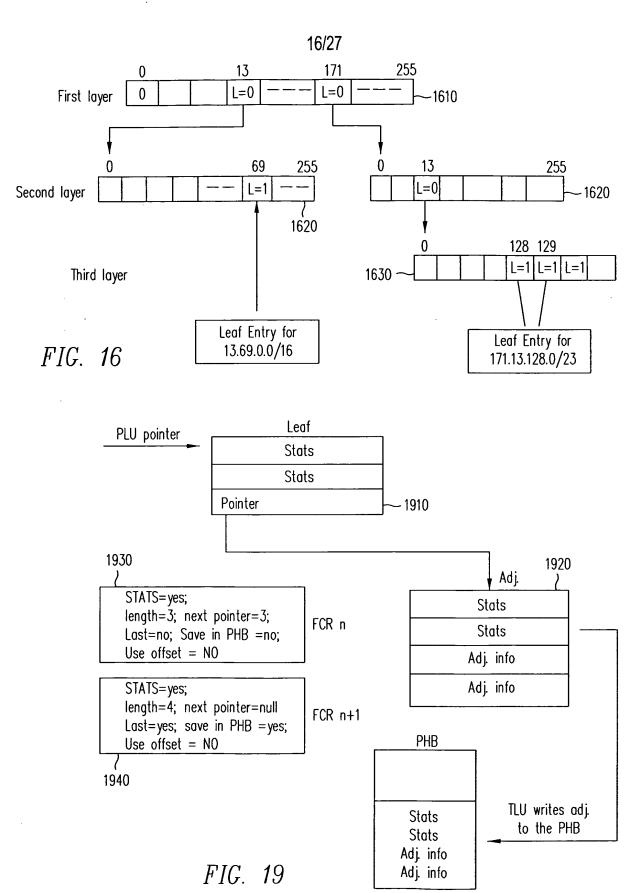








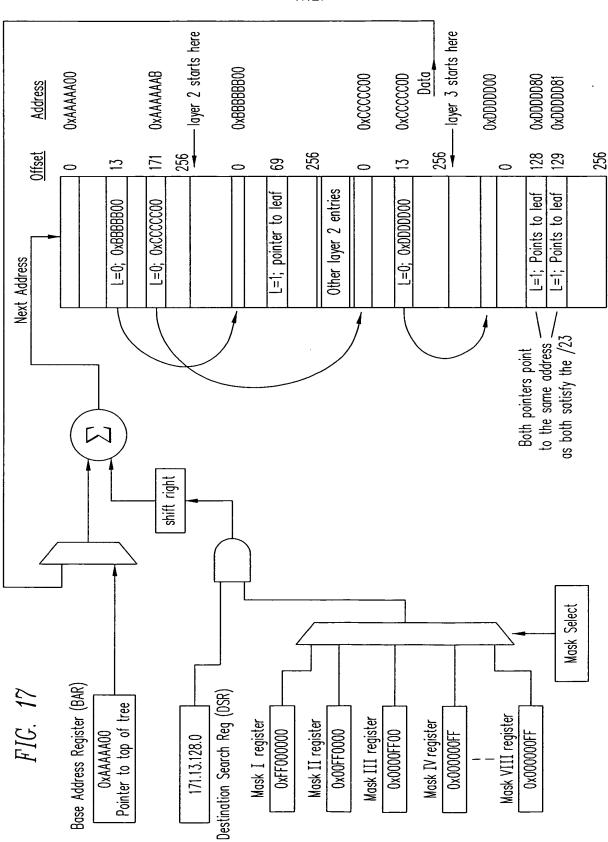
Title: Pipelined Packet Switching And Queuing Architecture





Title: Pipelined Packet Switching And Queuing Architecture

REPLACEMENT SHEET

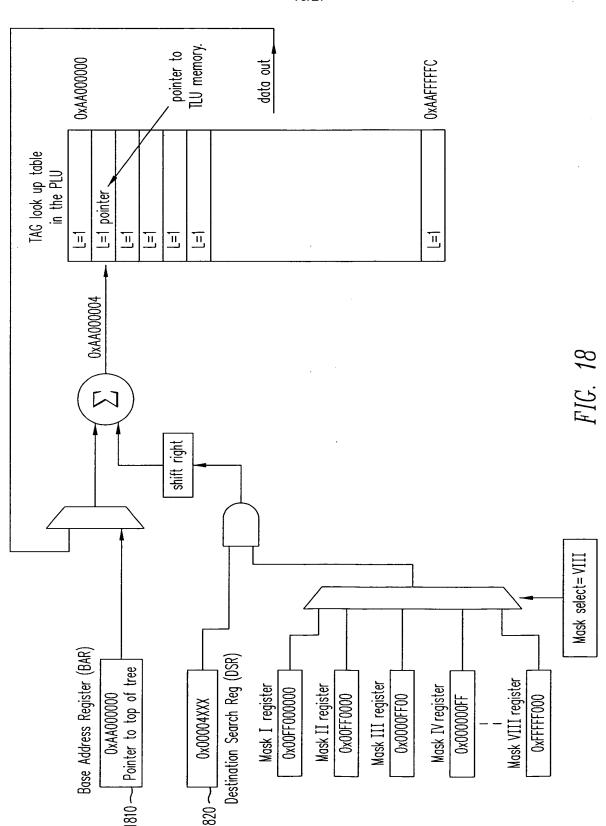




Application Serial No.: 09/503,442

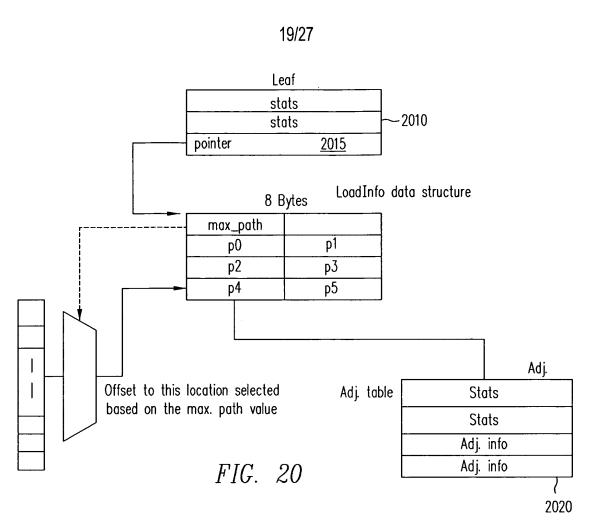
First Named Inventor: Garry P. Epps
Title: Pipelined Packet Switching And Queuing Architecture

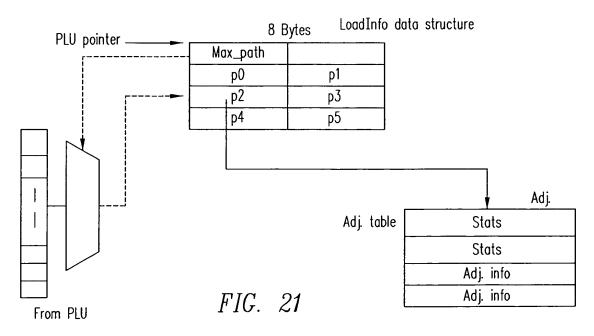
REPLACEMENT SHEET





Title: Pipelined Packet Switching And Queuing Architecture







Title: Pipelined Packet Switching And Queuing Architecture

REPLACEMENT SHEET

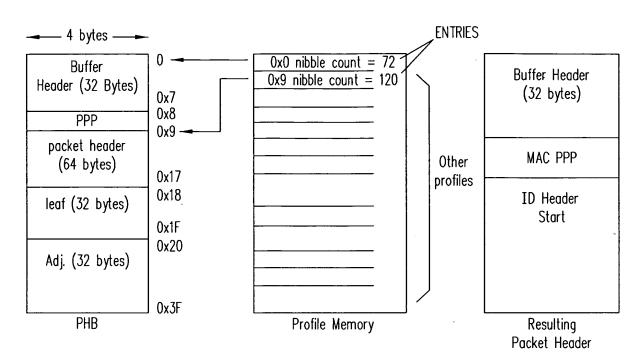
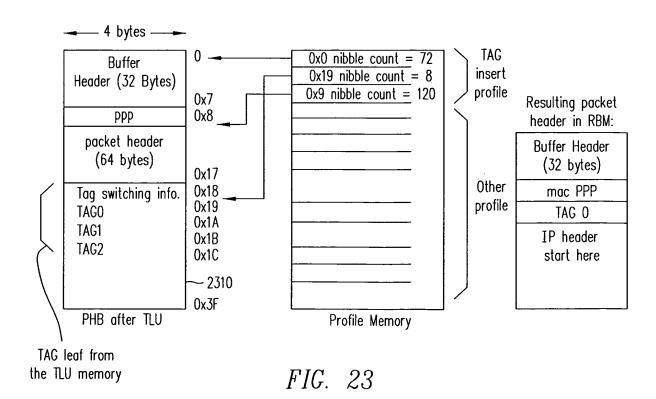


FIG. 22





Title: Pipelined Packet Switching And Queuing Architecture

REPLACEMENT SHEET

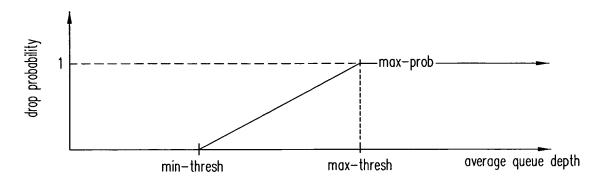


FIG. 24

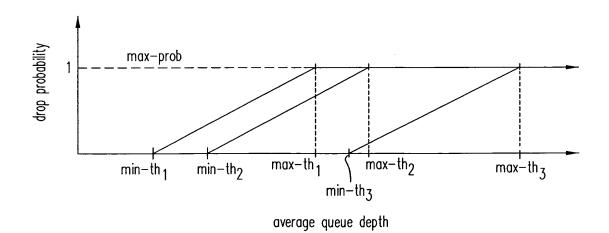
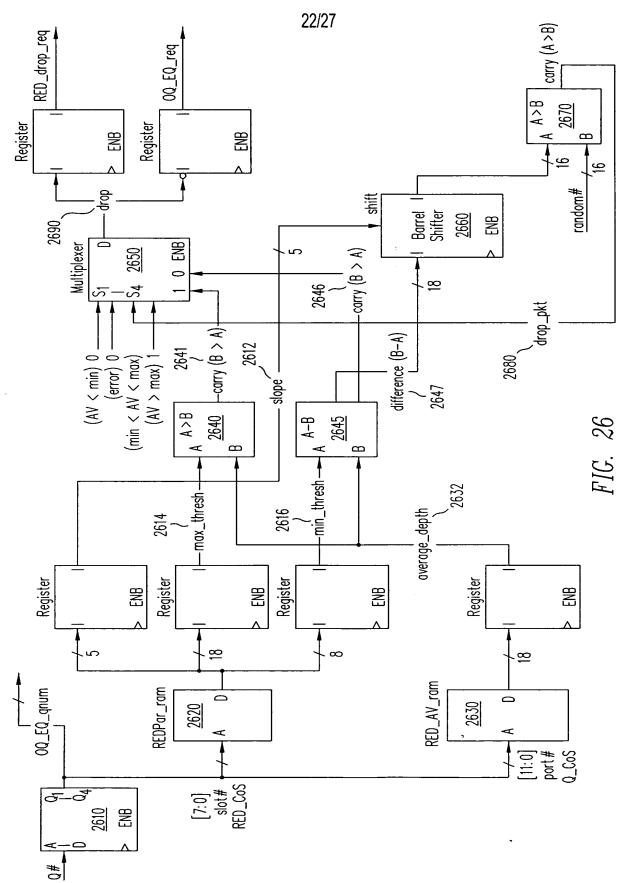


FIG. 25



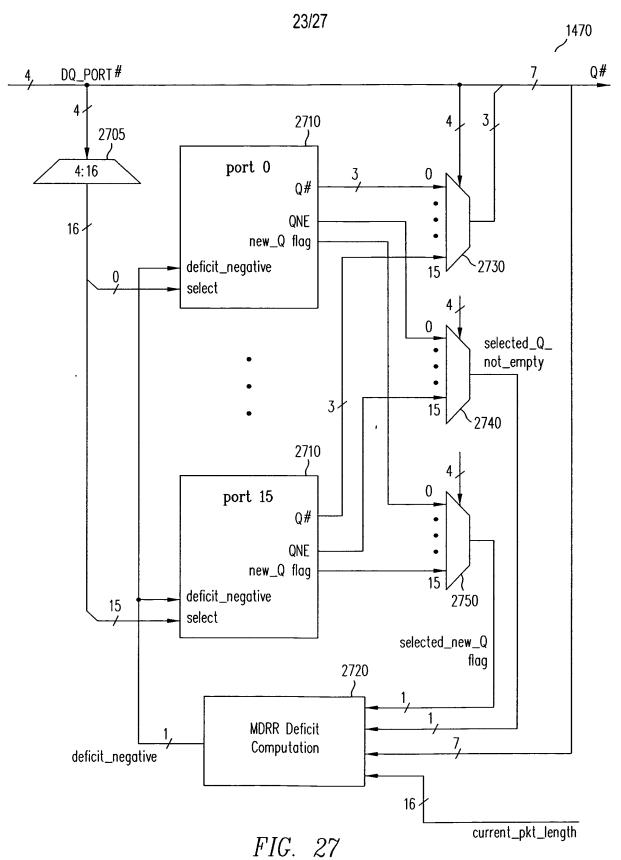
Title: Pipelined Packet Switching And Queuing Architecture





Application Serial No.: 09/503,442

First Named Inventor: Garry P. Epps
Title: Pipelined Packet Switching And Queuing Architecture





Title: Pipelined Packet Switching And Queuing Architecture

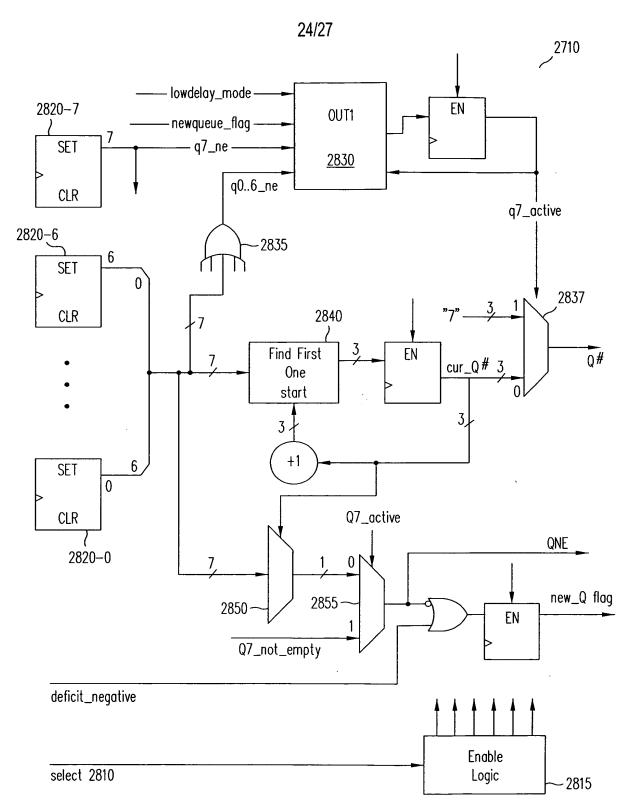
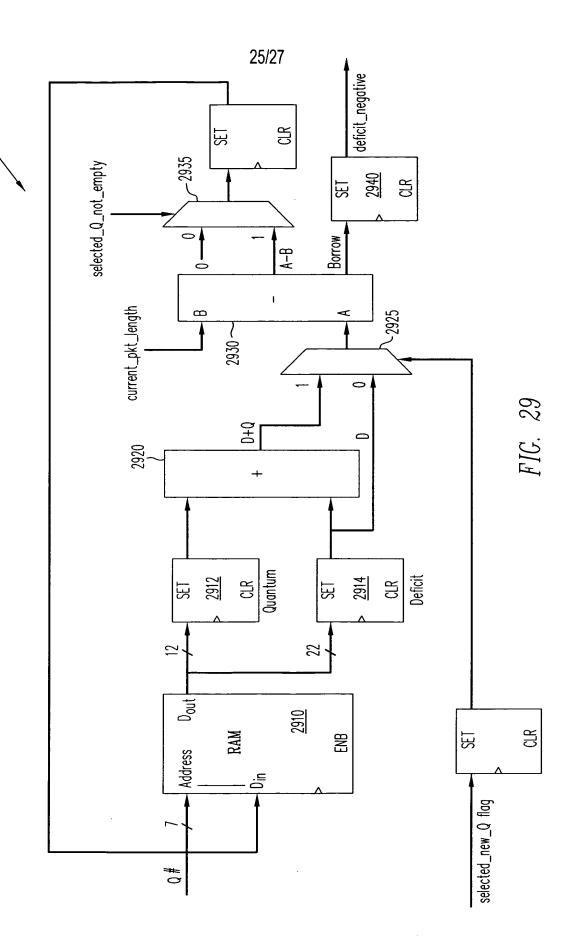


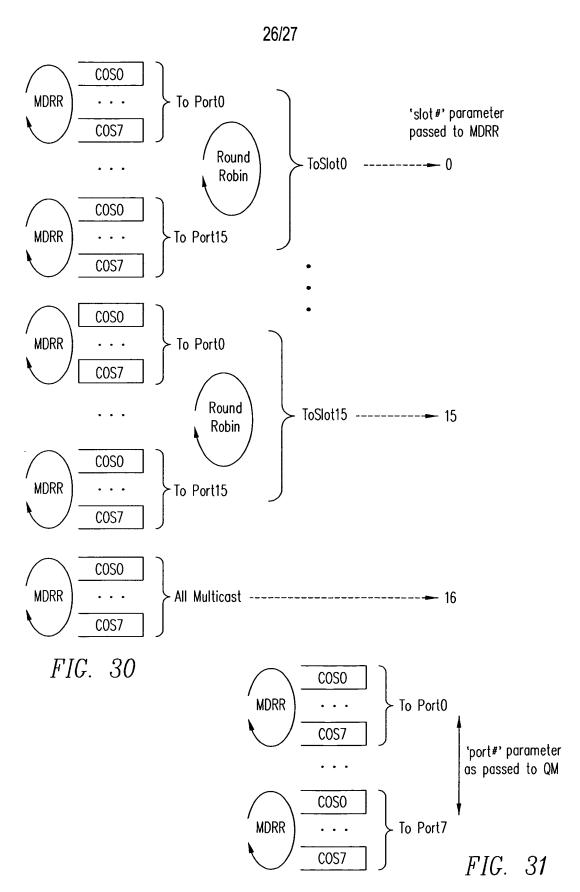
FIG. 28







Title: Pipelined Packet Switching And Queuing Architecture





27/27

